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Scheme - E  
Sample Question Paper

Course Name : Diploma in Computer Engineering	
Course Code : C04CD	<b>12181</b>
Semester : Fifth	
Subject Title : Advance Microprocessor	
Duration : 3 Hours	Marks : 100

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Instructions:

- 1) All questions are compulsory.
- 2) Illustrate your answers with neat sketches wherever necessary.
- 3) Figures to the right indicate full marks.
- 4) Assume suitable data if necessary.
- 5) Preferably, write the answers in sequential order.

Q.1 (a) Attempt any THREE of the following. 12 Marks

- a) List the four important features of 80286.
- b) Describe the control register structure of microprocessor 80386.
- c) Describe the interrupt processing sequence for software interrupts.
- d) List the salient features of 80486(any four)

Q.1 (b) Attempt any ONE of the following. 06 Marks

- a) Explain the flag register format of 80286 with a suitable figure.
- b) With suitable diagram describe the real addressing mode of microprocessor 80386.

Q.2 Attempt any TWO of the following. 16 Marks

- a) With a neat labeled diagram explain the internal architecture of 80286.
- b) With a suitable figure describe the address calculation procedure in Protected Virtual Addressing Mode of microprocessor 80286.
- c) With a suitable figure describe the paging mechanism of microprocessor 80386 without Translation Lookaside Buffer.

Q.3 Attempt any FOUR of the following. 16 Marks

- a) Draw and discuss the structure of a code descriptor in 80286 microprocessor.
- b) Illustrate with diagram the four level privilege mechanism in microprocessor 80286.
- c) With neat diagram, describe gate descriptor of 80386 microprocessor.

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